

Please type a plus sign (+) inside this box → ☒

PTO/SB/50 (08-00)

Approved for use through 12/30/2000. OMB 0651-0033  
U.S. Patent and Trademark Office U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number

## REISSUE PATENT APPLICATION TRANSMITTAL

Address to:

Assistant Commissioner for Patents  
Box Patent Application  
Washington, DC 20231

Attorney Docket No.	93-C-032RE (1678-42)
First Named Inventor	Alex Kalnitsky
Original Patent Number	5,986,330
Original Patent Issue Date (Month/Day/Year)	November 16, 1999
Express Mail Label No.	ET564725085US

### APPLICATION FOR REISSUE OF:

(check applicable box)

☒ Utility Patent

☐ Design Patent

☐ Plant Patent

#### APPLICATION ELEMENTS (37 CFR 1.173)

- ☐ \* Fee Transmittal Form (e.g., PTO/SB/56)  
(Submit an original, and a duplicate for fee processing)
- ☐ Applicant claims small entity status. See 37 CFR 1.27.
- ☒ Specification and Claims in a double column copy of patent format (amended, if appropriate)
- ☒ Drawing(s) (proposed amendments, if appropriate)
- ☒ Reissue Oath / Declaration (original or copy)  
(37 C.F.R. § 1.175)(PTO/SB/51 or 52)
- Original U.S. Patent currently assigned?  
☒ Yes ☐ No

(If Yes, check applicable box(es))

☐ Written Consent of all Assignees (PTO/SB/53)

☒ 37 C.F.R. § 3.73(b) Statement ☐ Power of Attorney  
(PTO/SB/96)

#### ACCOMPANYING APPLICATION PARTS

- ☐ Statement of status/support for all changes to the claims. See 37 CFR 1.173(c).
- ☐ Original U.S. Patent for surrender  
☐ Ribbioned Original Patent Grant  
☐ Statement of Loss (PTO/SB/55)
- ☐ Foreign Priority Claim (35 U.S.C. 119)  
(if applicable)
- ☐ Information Disclosure Statement (IDS)/PTO-1449 ☐ Copies of IDS Citations
- ☐ English Translation of Reissue Oath/Declaration  
(if applicable)
- ☒ Preliminary Amendment
- ☒ Return Receipt Postcard (MPEP 503)  
(Should be specifically itemized)
- ☒ Other: Assent of Assignee; Certificate of Express Mail

### 14. CORRESPONDENCE ADDRESS

☒ Customer Number or Bar Code Label



or ☐ Correspondence address below

Name			
Address			
City	State	Zip Code	
Country	Telephone	Fax	

NAME (Print/Type)	Bryan A. Santelli	Registration No. (Attorney/Agent)	37,560
Signature		Date	11/16/01

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Box Patent Application, Washington, DC 20231

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Original Patent

Patentees: Alex Kalnitsky  
Yih-Shung Lin

Patent No.: 5,986,330

Title: ENHANCED PLANARIZATION  
TECHNIQUE FOR AN  
INTEGRATED CIRCUIT

Issued: November 16, 1999

Atty Dk No.: 93-C-032C3

Reissue Application

Applicants: Alex Kalnitsky  
Yih-Shung Lin

Serial No.:

Title: ENHANCED PLANARIZATION  
TECHNIQUE FOR AN  
INTEGRATED CIRCUIT

Filing Date: November 16, 2001

Atty Dk No.: 93-C-032RE (1678-42)

CERTIFICATE OF MAILING OR TRANSMISSION

"Express Mail" mailing label number: ET564725085US

Date of Deposit: November 16, 2001

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR, Section 1.10 on the date indicated above and is addressed to: Commissioner for Patents, Box PATENT APPLICATION, Washington, D.C. 20231 by

  
Signature

## ASSENT OF ASSIGNEE

TO THE ASSISTANT COMMISSIONER FOR PATENTS:

STMicroelectronics, Inc., assignee of U.S. Patent No. 5,986,330, consents to the filing of reissue application No. \_\_\_\_\_ (or the present application, if filed with the initial application papers) for the reissue of U.S. Patent No. 5,986,330.

STMicroelectronics, Inc.

---

Lisa K. Jorgenson  
Director of Intellectual Property  
Assistant Secretary

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Original Patent

Patentees: Alex Kalnitsky  
Yih-Shung Lin

Patent No.: 5,986,330

Title: ENHANCED PLANARIZATION  
TECHNIQUE FOR AN  
INTEGRATED CIRCUIT

Issued: November 16, 1999

Atty Dk No.: 93-C-023C3

Reissue Application

Applicants: Alex Kalnitsky  
Yih-Shung Lin

Serial No.:

Title: ENHANCED PLANARIZATION  
TECHNIQUE FOR AN  
INTEGRATED CIRCUIT

Filing Date: November 16, 2001

Atty Dk No.: 93-C-023RE (1678-42)

CERTIFICATE OF MAILING OR TRANSMISSION

"Express Mail" mailing label number: ET564725085US

Date of Deposit: November 16, 2001

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR, Section 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents, Box PATENT APPLICATION, Washington, D.C. 20231 by

  
Signature